

CURRICULUM VITAE

CHHAVI KUMAR BHARTI

M.Tech,B.Tech, NET, GATE,
(Male,27 years,General, Rajput, Indian)
chhavirajchauhan@gmail.com
+919634803114, +918630055112



Career Objective:

To utilize my skills and capabilities to the fullest reflecting in my personal growth in conjunction with organization's goal and objectives

Work Responsibilities:

S.N	Organization	Designation	Pay Scale/Salary	Duration
1	ARSD College (Delhi University)	Assistant Professor (Ad-hoc)	15600-39100 GP-6000	04 Jan 17 -03 May 17
2	ARSD College (Delhi University)	Assistant Professor (Ad-hoc)	15600-39100 GP-6000	15 Sept16 -28 Nov 16
3	IIT Delhi	Project Associate	15000/-	01 Sept15- 31 Aug 16

Educational Qualifications:

Examination	Discipline/ Specialization	School/ Collage	Board/ University	Year of Passing	%
M.TECH	Digital Communication	BTKIT Dwarahat(An Autonomous institute of Govt of Uttarakhand)	UTU Dehradun	2015	79.75
B.TECH	Electronics & Communication	BIT Muzaffarnagar	GBTU(UPTU)	2012	68.64
12 th	PCM	DIC Morna, Bijnor	UP Board	2007	65.00
10 th	Science	DIC Morna, Bijnor	UP Board	2005	53.83

Achievements:

1. UGC NET(LS) Nov 2017,JUN 2015 &DEC 2014
2. GATE 2013-2017(5 Times)

Subject Taught and Labs:

1. Signal and System
2. Digital Electronics and VHDL
3. Design and Fabrication of Printed Circuit Boards
4. Fundamental of Semiconductor Fabrication
5. Internet and Java Programming
6. VHDL programming
7. Digital Electronics and VHDL Practical Lab
8. Analog Communication Practical Lab
9. Semiconductor Fabrication Simulation Lab
10. Optics Practical Lab

Project/Thesis:

1. M.Tech dissertation titled, “ Design of **OTRA Based Fractional Order Device of Order $\pm 1/2$** ”, under the guidance of Dr. Ashish Ranjan(At present HOD ECE dept, NIT Manipur).
2. B.Tech Final Year project on, “Power Line Communication” Under the guidance of Mr. Vibhor Bhardwaj.

Publication:

Research Paper:

1. **Chhavi Kumar Bharti**, Shelender Singh, Ashish Ranjan, “Application of fractional calculus in analog signal processing circuits,” International journal of advanced research in computer and communication engineering, pp.307-309, vol.4, 2015.

Proceedings:

1. Shelender Singh, **Chhavi Kumar Bharti**, Ashish Ranjan , “Second Generation Current Conveyor Based Colpitt Oscillator,” 2nd IEEE international conference on Advanced in Computing and Communication 1-2 May 2015

Book :

1. “Energy Technology for Clean Environment” ,**Chhavi Kumar Bharti**, Pawan Kumar Bharti , Discovery Publishing House ,New Delhi ,ISBN 93-86841-20-3, 2018
2. “Geo-spatial technology and Ecosystem Assesment ” ,**Chhavi Kumar Bharti**, Alka Chauhan,Pawan Kumar Bharti , Discovery Publishing House New Delhi , ISBN 93-86841-36-3, 2018.

Seminar/Conferences presentation/attended:

1. Chhavi Kumar Bharti, Pawan Kumar “Power Line Communication Application And Future Scope” National Conference on Advance in Engineering and technology, BIT M.Nagar, 16-17 April 2012.
2. Chhavi Kumar Bharti, “Smart Home Area Network Device” National Conference on Advance in Engineering and technology , BIT M.Nagar, 16-17 April 2012.
3. Chhavi Kumar Bharti, Presented the project, “Power Line Communication” in A National Level Project Competition, held at BIT Muzaffarnagar on 31th May 2012.
4. Attended national workshop cum conference for Farmers on “Organic Farming and technologies’ at Village Jamaalpur Maan (Bijnore) on 15 November 2015.

Workshop/Courses/Training:

1. A short term course on “Advanced Analog Signal Processing And Generating Circuits” at ISM Dhanbad during 24-28 march 2014.
2. IEEE workshop on Hand on practice “Microwave Integrated Circuits Fabrication and Measurement”, held on 8 oct 2011 at BIT M.Nagar.
3. Six-week Industrial Training from BHEL HARIDWAR

Extra Curricular:

1. Active Volunteer and member of **Sehat india**(An NGO working in the field of Environment, Agriculture and Ecology)
2. Worked as Volunteer in International conference at BTKIT,Dwarahat during 22-23 feb2014.
3. Presented the project entitled, “Power Line Communication” in A National Level Project Competition, held on 31th May 2012.

Computer Skills/Software:

Skills	Details
Orcad v10.5	An Electronics Software, used to simulate Analog Circuits
Xilinx ISE 14.7	A VHDL Software
EAGLE PCB	A PCB Design software
Eclipse Java Neon	A java programming Software
Matlab	A Software for mathematics and signal processing
MS Office	An Application Software
Network Simulator 3	Open source platform for Researchers with C++ library
Window 7,8, Ubuntu	Operating System

Personal Details:

- Name Chhavi Kumar Bharti
- Father's Name Dr. Jayram Singh
- Sex Male
- Marital Status Single
- Religion Hindu
- Nationality Indian
- Hobbies Motivating Children & Youngsters, Helping needy
- Permanent Address Vill- Jamaalpur Maan, Post- Raja Ka Tajpur, Bijnor, 246735, U.P.
- Date of Birth 21-July 1990

References:

1. Dr. Monika Aggrawal Associate Professor, CARE, IIT Delhi. Email- maggarwal@care.iitd.ac.in Phone- 011 26591113	2. Dr. Meena Dadu Associate Professor ARSD Collage(Delhi University) Email- meenadadu1970@rediffmail.com Mob-9810621659
3. Mrs Saruchi Tandon Associate professor ARSD Collage(Delhi University) Email- Saruchitandon1970@gmail.com Mob-9810889096	4. Dr. Ashish Ranjan Head Of Department ECE Dept, NIT Manipur Email- ashish.ece@nitmanipur.ac.in Mob-9471173367

Declaration:

I hereby declare that the above mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above mentioned particulars.

Chhavi Kumar Bharti
Name/Signature